

FIG. 1

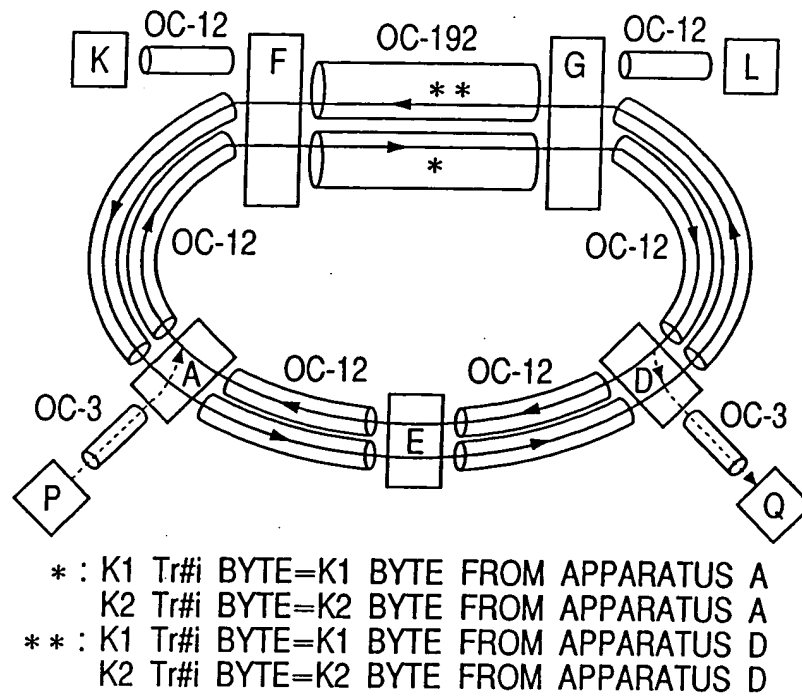


FIG. 2

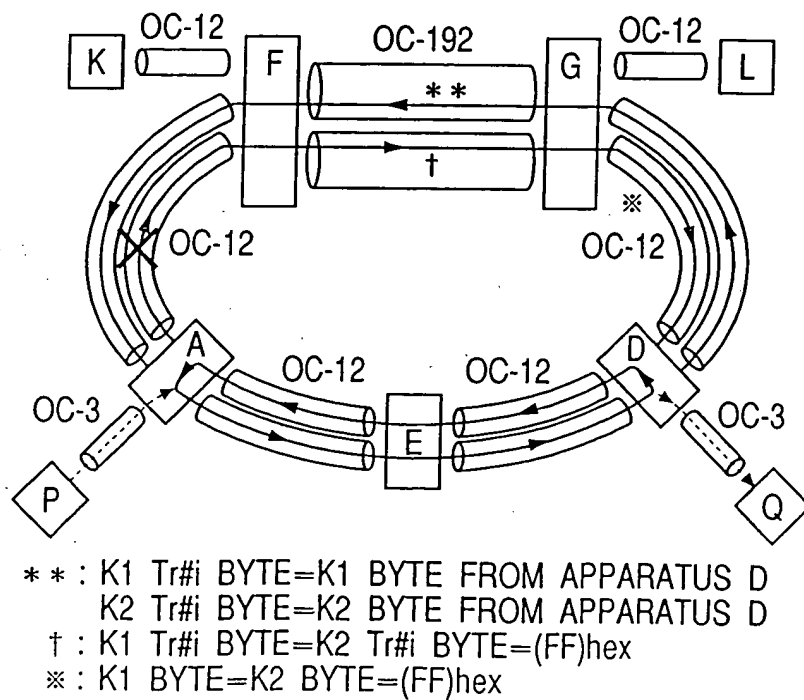
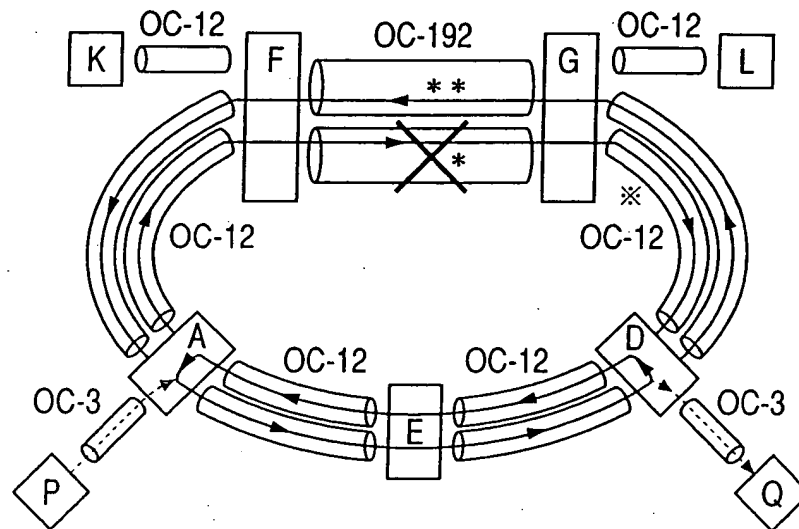


FIG. 3



* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A

** : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

⌘ : K1 BYTE=K2 BYTE=(FF)hex

FIG. 4

OVERHEAD		FUNCTION
SECTION OVERHEAD	A1, 2A	FRAME SYNCHRONIZATION
	B1	ADMINISTRATION OF ERRORS IN SECTION INTERVAL
	D1~D3	USED FOR MAINTENANCE OPERATION
	E1	SPEECH COMMUNICATION FOR CRAFT MAN
	J0 (C1)	DESIGNATION OF OC-1 MULTIPLEX NUMBER OF OC-M SIGNAL
	F1	USED FOR CONVENIENCE OF CRAFT MAN
	H1, H2	INDICATION OF HEAD PHASE OF PATH
LINE OVERHEAD	H3	USED FOR FREQUENCY SYNCHRONIZATION
	B2, M1	ADMINISTRATION OF ERRORS IN LINE INTERVAL
	K1, K2	SWITCHING CONTROL FOR LINE INTERVAL, TRANSFER OF ALARM
	D4~D12	USED FOR MAINTENANCE OPERATION
	E2	SPEECH COMMUNICATION FOR CRAFT MAN
	S1	USED FOR OPERATION OF NETWORK SYNCHRONIZATION
	Z1, Z2	INTERNATIONALLY RESERVED AS SPARE

[illegible]

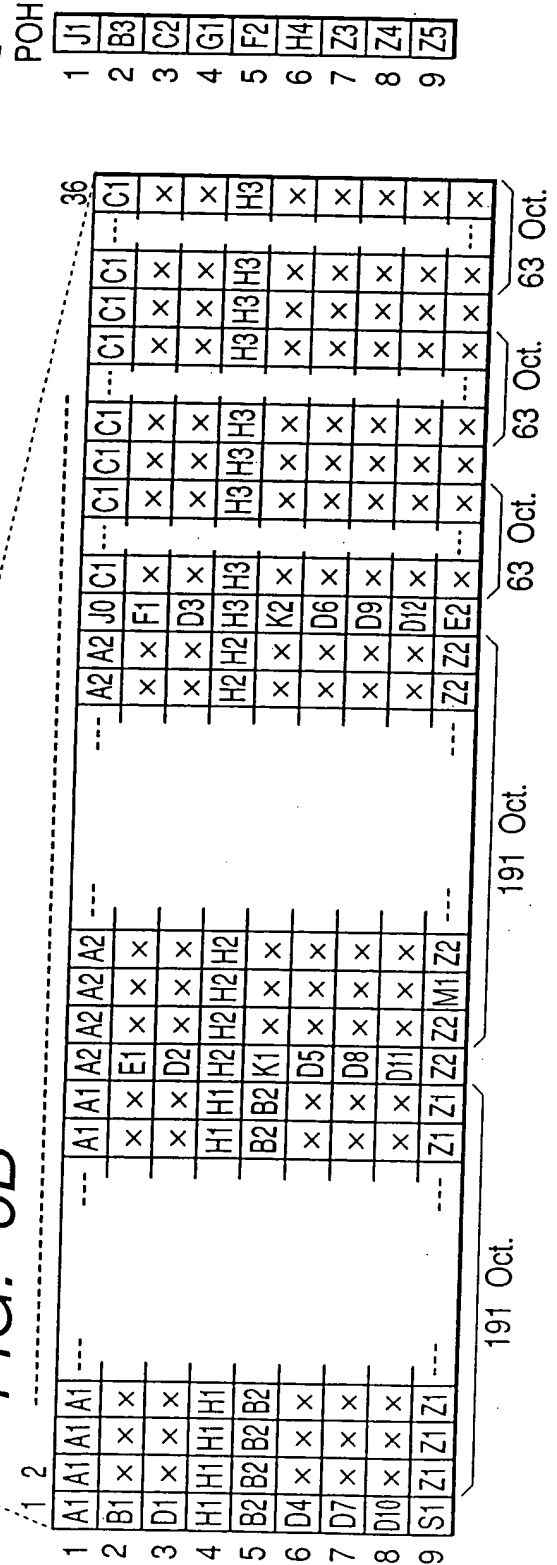
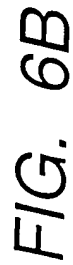


FIG. 7

FIG. 7

10G FRAME				192 BYTE				192 BYTE				192 BYTE				87×192 BYTE			
64 BYTE		64 BYTE		64 BYTE		64 BYTE		64 BYTE		64 BYTE		64 BYTE		64 BYTE		64 BYTE			
1	A1	A2	A1	A2	A2	J0	C1	C1		PAYLOAD		
2	B1	E1	F1		PAYLOAD		
3	D1	D2	D3		PAYLOAD		
4	H1	H1	H1	H2	H2	H2	H3	H3	H3		PAYLOAD		
5	B2	B2	B2	K1	Trb. k1	1100	...	K2	Trb. k2		PAYLOAD		
6	D4	Trb. D4	1000	...	D5	Trb. D5	Trb. E1	D6	Trb. D6	Trb. F1		PAYLOAD		
7	D7	Trb. D7	Trb. D1	...	D8	Trb. D8	Trb. D2	D9	Trb. D9	Trb. D3		PAYLOAD		
8	D10	Trb. D10	D11	Trb. D11	Trb. B2	D12	Trb. D12	Trb. M1		PAYLOAD		
9	S1	Z1	Z1	Z2	Z2	Z2	Trb. E2		PAYLOAD		

(a)
(b)
(c)

**LOCATIONS ALLOCATED TO
THROUGH BYTES**

FIG. 8

	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(BYTE)
1	A1	A1	A1	A1	A1	A1	...	A1	A1	...	A1	...	A1	A1	A1	A1	...
2	B1					
3	D1					
4	H1	H1	H1	H1	H1	H1	...	H1	H1	...	H1	...	H1	H1	H1	H1	...
5	B2	B2	B2	B2	B2	B2	...	B2	B2	...	B2	...	B2	B2	B2	B2	...
6	D4						...	D4		...	D4	...	D4				...
7	D7						...	D7		...	D7	...	D7				...
8	D10						...	D10		...	D10	...	D10				...
9	S1	Z1	Z1	Z1	Z1	Z1	...	Z1	Z1	...	Z1	...	Z1	Z1	Z1	Z1	...

FIG. 9

	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(BYTE)
1	A2	A2	A2	A2	A2	A2	...	A2	A2	A2	A2	...	A2	A2	A2	A2	...
2	E1					
3	D2					
4	H2	H2	H2	H2	H2	H2	...	H2	H2	H2	H2	...	H2	H2	H2	H2	...
5	K1						...	K1				...	K1				...
6	D5						...	D5				...	D5				...
7	D8						...	D8				...	D8				...
8	D11						...	D11				...	D11				...
9	Z2	Z2	M1	Z2	Z2	Z2	...	Z2	Z2	Z2	Z2	...	Z2	Z2	Z2	Z2	...

FIG. 10

	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(BYTE)
1	J0	C1	C1	C1	C1	C1	...	C1	C1	...	C1	...	C1	C1	C1	C1	...
2	F1					
3	D3					
4	H3	H3	H3	H3	H3	H3	...	H3	H3	...	H3	...	H3	H3	H3	H3	...
5	K2						...	K2		...	K2
6	D6						...	D6		...	D6
7	D9						...	D9		...	D9
8	D12						...	D12		...	D12
9	E2						...	E2		...	E2

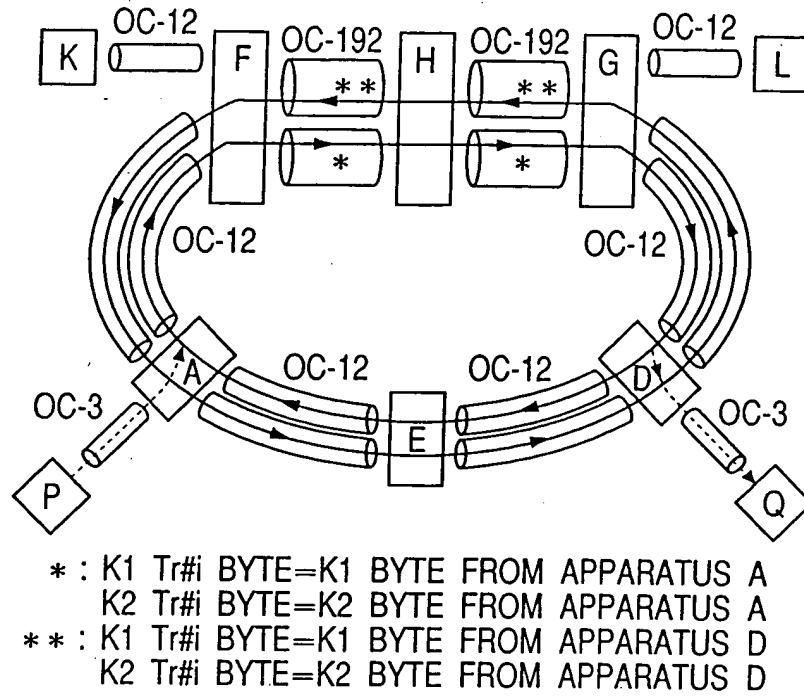
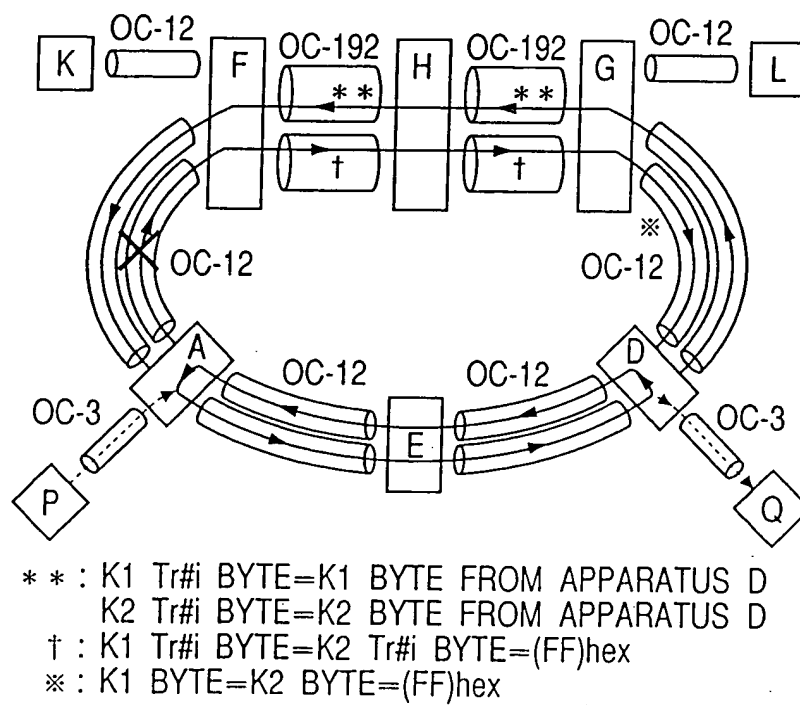
FIG. 11**FIG. 12**

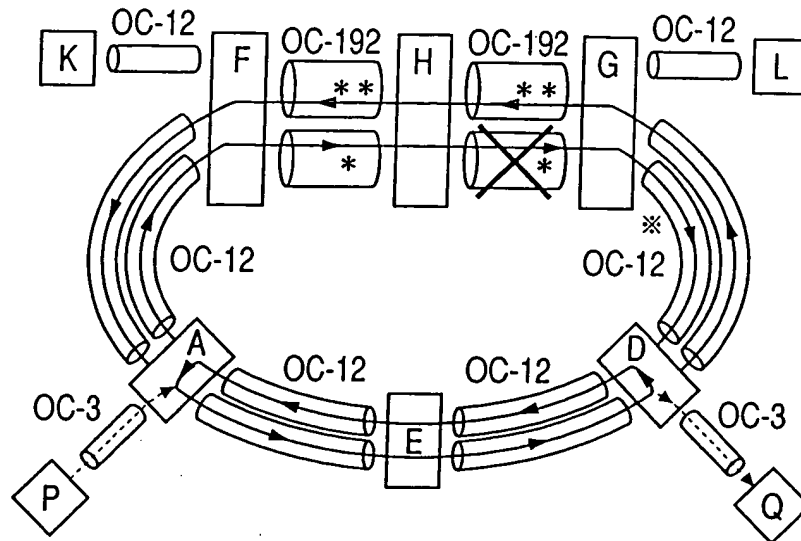
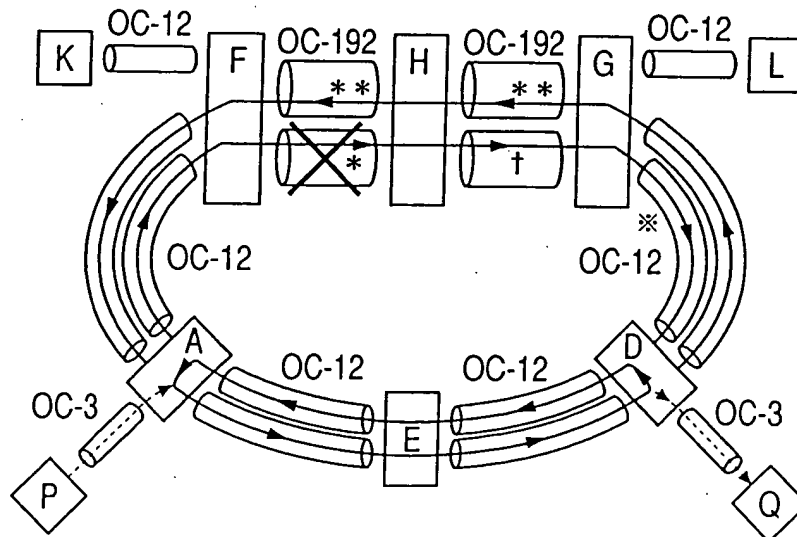
FIG. 13**FIG. 14**

FIG. 15

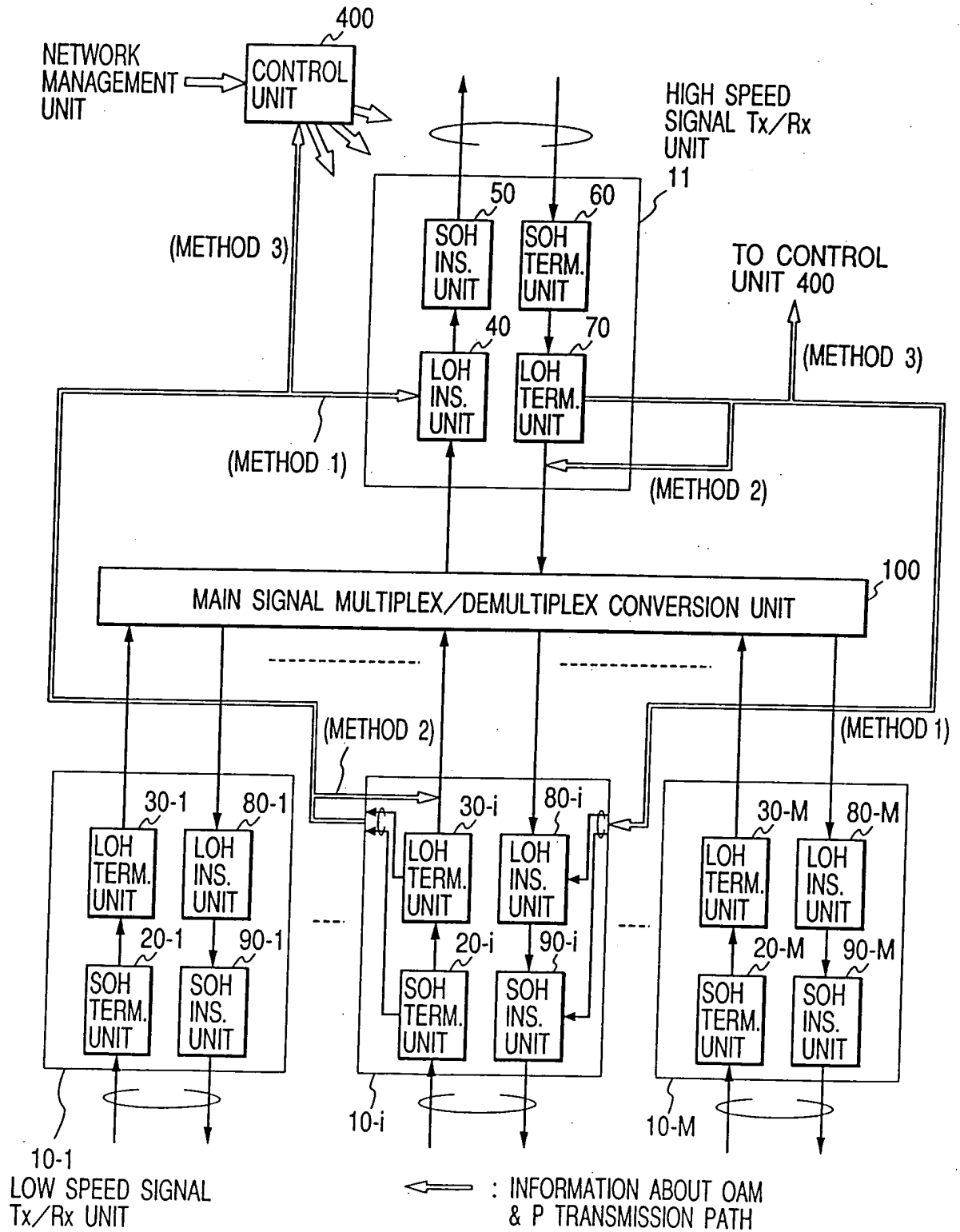


FIG. 16

